THIS IS AN **OPEN** BOOK/OPEN NOTES QUIZ – NO ELECTRONIC DEVICES

(10 points)

(2) Give the DIRECT ADDRESSING mode instruction to load the B accumulator (mnemonic LDAB) from memory location $0014.

LDAB $14

(2) What addressing mode is the following instruction? LDAA #$14

IMMEDIATE

(2) For LDAA #$14 what value is loaded into the A accumulator? \_\_\_\_\_\_\_ $14 \_\_\_\_\_\_

(give answer in hexadecimal)



(2) What is the 2’s complement of $22? \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

$22 is 0010 0010

2’complement is 1101 1110

1’s complement 1101 1101

(2) In EXTENDED ADDRESSING mode how many bits of the address of the operand are given with the instruction? \_\_\_\_\_ 16 bits or 2 bytes \_\_\_\_\_\_\_