Embedded System Design Domains

Mixed Signal Electronic Systems (MISES)  
System on a Chip (SoC) Development

Steven Bibyk

Software

User Firmware

User Microcode

SoC EMBEDDED IMPLEMENTATION

Requirements Specification

Algorithms & Behavior

LIBRARIES

hardware

Control Path

Data Path

Signal Path

RF/Analog Path

Digital

User Software

User Platform and OS

DataFlow Design

Classes

Embedded Platform and RTOS

ALU

Busses

Registers

Logic Design

I/O Drivers/ Buffers

Passives

Transistors

RF

Analog/Power

I/O & D/A

Analog

PA

LNA

Mixers

VCOs

IF

Other

Antenna

Mechanical

Mec

ha

cal
Low Cost Radar Sensor Node in a Sensor Network